L	Hits	Search Text	DB	Time stamp
Number				
29	53	(hardware adj interrupt) same (software	USPAT; US-PGPUB;	2004/09/02 19:52
		adj interrupt) same priority	EPO; JPO;	19.32
			DERWENT;	
			IBM_TDB	
30	3	(hardware adj interrupt) same (software	USPAT;	2004/09/02
		adj interrupt) same priority same fix\$3	US-PGPUB;	19:52
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
31	59	(hardware peripheral external i/o	USPAT;	2004/09/02
		input/output) near2 interrupt same	US-PGPUB;	19:54
		(software internal program) same priority	EPO; JPO;	
		same fix\$3	DERWENT;	
			IBM_TDB	
32	1426	710/260-266.ccls.	USPAT;	2004/09/02
			US-PGPUB	20:11
33	14	((hardware adj interrupt) same (software	USPAT;	2004/09/02
		adj interrupt) same priority) and	US-PGPUB	20:11
		710/260-266.ccls.		

			TTO D D M	2002/02/14
_	0	(interrupt adj controller) and (interrupt	USPAT;	2003/03/14 17:37
		adj register) and (select\$3 assign\$4	US-PGPUB	17:37
		choos\$3) near10 (between adj priority)	man a m	2002/02/14
_	20	(interrupt adj controller) and (interrupt	USPAT;	2003/03/14
		adj register) and ((select\$3 assign\$4	US-PGPUB	18:12
		choos\$3) near10 priority) and (round adj		
		robin)		
_	0	(latch near6 circuit) and	USPAT;	2003/03/14
		(710/260-266.ccls. and ((hardware near5	US-PGPUB	18:13
		interrupt) same (software near5		
		<pre>interrupt) same (reg\$1 or register\$1)))</pre>		
		same (round adj robin)		
_	69	(hardware near5 interrupt) same (software	USPAT;	2003/03/14
		near5 interrupt) same (reg\$1 or	US-PGPUB	18:14
		register\$1) and (round adj robin)		
_	11	(hardware near5 interrupt) same (software	USPAT;	2003/12/01
		near5 interrupt) same (reg\$1 or	US-PGPUB	13:13
		register\$1) and (round adj robin) and	1	
		(equal adj priority)		j l
_	1	(hardware adj interrupt) same (software	USPAT;	2003/12/01
	_	adj interrupt) same priority and (round	US-PGPUB	13:14
		adj robin) and (equal adj priority)		
_	1		USPAT;	2003/12/01
	1	adj interrupt) same priority and (round	US-PGPUB;	13:15
		adj robin) and (equal adj priority)	EPO; JPO;	
		auj lobin, and (equal ad) prioricy,	DERWENT;	ļ.
			IBM TDB	
	2	(hardware adj interrupt) same (software	USPAT;	2003/12/01
_		adj interrupt) same priority and (round	US-PGPUB;	14:11
			EPO; JPO;	1 1 1 1 1 1
		adj robin)	DERWENT;	
			IBM TDB	
	4.0	// where all introducts are /goftware	USPAT;	2004/09/02
-	48		US-PGPUB;	19:52
		adj interrupt) same priority	EPO; JPO;	19.32
			DERWENT;	
	_	0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IBM_TDB	2004/03/29
_	6		USPAT;	11:19
		adj interrupt) and (priority and robin)	US-PGPUB;	11:19
			EPO; JPO;	
ł			DERWENT;	
1	1		IBM_TDB	2004/02/20
-	10	1 3 - 1	USPAT;	2004/03/29
1	1	US-5987601-\$ or US-6397284-\$ or	US-PGPUB	11:18
1	1	US-5701495-\$ or US-6412081-\$ or		
1		US-5459872-\$ or US-5822595-\$ or		
1		US-5937199-\$).did. or		
		(US-20030037228-\$).did.		
_	52		USPAT;	2004/03/29
		adj interrupt) same priority	US-PGPUB;	11:19
			EPO; JPO;	
			DERWENT;	
1	1	I and the second	IBM TDB	1

	2002/10/23
(interrupt adj register)) and (stor\$4 adj US-PGPUB	09:27
device) and ((hardware and software) same	
request)	
	2002/10/23
(09:30
(\timestrupe aa) regreeor/, and \maranare (or rere	03.30
and software) and (stor\$4 near6 request)	2002/10/22
	2002/10/23
\\\(\frac{1}{21100114}\) \(\frac{1}{20}\) \(\frac{1}{20	09:30
and software) near6 request) and (stor\$4	
near6 request)	
- 35 ((interrupt adj controller) and USPAT;	2002/10/23
(interrupt adj register)) and ((hardware US-PGPUB	10:33
and software) same request) and (stor\$4	
near6 request)	
	2003/03/14
	16:55
adj interrupt)	2002/10/23
o (maranaro mone)	' '
110010 11100110110 (119)	10:35
register\$1) and 710/260.ccls	
	2002/10/23
near5 interrupt) same (reg\$1 or US-PGPUB	10:35
register\$1) and 710/260-266.ccls	
- 297 (hardware near5 interrupt) same (software USPAT;	2003/03/14
25, /11424/425 114425 114425 1	18:13
register\$1)	
	2002/10/25
12 /10/200 200:001B: and //iidaa.mater 00-11-1	16:18
	10.10
interrupt) same (reg\$1 or register\$1))	2002/10/22
11 00047 - 1111-1111-1111-1111-1111-1111-1111-	2002/10/23
(110/200 200100101 4110 (1110 1110)	10:57
controller) and (interrupt adj	
register)))	
	2002/10/25
1 10000 1 1100011 010 1	16:18
	2002/10/25
00 110/200 2000000000000000000000000000000	16:18
interrupt) same (software nears interrupt) same (reg\$1 or register\$1))	
	2002/10/25
	· · · · · · · · · · · · · · · · · · ·
	16:19
interrupt) same (reg\$1 or register\$1))	2002/12/25
o (Iddolf dd) Ollodlo, and	2002/10/25
(/+0/200 D00:001B; Will //Bill # # # # # # # # # # # # # # # # # #	16:19
interrupt) same (software near5	
interrupt) same (reg\$1 or register\$1)))	
- 6 (latch near6 circuit) and USPAT;	2003/03/14
(710/260-266.ccls. and ((hardware near5 US-PGPUB	18:12
interrupt) same (software near5	
interrupt) same (software hears interrupt) same (reg\$1 or register\$1)))	
	2002/10/28
- 1 ("6397284").PN. USPAT; US-PGPUB	08:45
- 15 (hardware adj interrupt) same (software USPAT;	2003/03/14
day incorrage, and ((promiser))	17:04
near5 assign\$4)	
- 0 (hardware adj interrupt) same (software USPAT;	2003/03/14
adj interrupt) same ((permanent\$3 fix\$3) US-PGPUB	17:00
near5 assign\$4)	
- 0 (hardware adj interrupt) same (software USPAT;	2003/03/14
adj interrupt) same ((permanent\$3 fix\$3) US-PGPUB	17:00
same assign\$4)	
	2003/03/14
1 (hardware adj interrupt) same USPAT; (nermanent\$3 fix\$3) near5 assign\$4) US-PGPUB	17:06
- 3 (hardware adj interrupt) same USPAT;	2003/03/14
((permanent\$3 fix\$3) same assign\$4) US-PGPUB	17:06
- 4 (interrupt adj controller) and (interrupt USPAT;	2003/03/14
adj register) and (select\$3 assign\$4 US-PGPUB	17:36
choos\$3) near10 (equal adj priority)	
- 119 (interrupt adj controller) and (interrupt USPAT;	2003/03/14
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adi register) and ((select\$3 assign\$4 US-PGPUB	18:10
adj register) and ((select\$3 assign\$4 US-PGPUB choos\$3) near10 priority)	18:10

L Number	Hits	Search Text	DB	Time stamp
29	53	(hardware adj interrupt) same (software adj interrupt) same priority	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/02 19:52
30	3	(hardware adj interrupt) same (software adj interrupt) same priority same fix\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/09/02 19:52
31	59	(hardware peripheral external i/o input/output) near2 interrupt same (software internal program) same priority same fix\$3	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/09/02 19:54
-	12443	710/\$.ccls.	USPAT	2004/03/30 09:27
_	261	(interrupt adj controller) and (interrupt adj register)	USPAT; US-PGPUB	2003/03/14
-	142 65	710/\$.ccls. and ((interrupt adj controller) and (interrupt adj register))	USPAT; US-PGPUB	2002/10/22 14:53
_	10	710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register)) (storage adj device) and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj	USPAT; US-PGPUB USPAT; US-PGPUB	2002/10/23 09:00 2002/10/22 14:58
_	1	register))) circuitry and ((storage adj device) and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj	USPAT; US-PGPUB	2002/10/22 14:59
-	64	register)))) stor\$4 and (710/260-266.ccls. and ((interrupt adj controller) and	USPAT; US-PGPUB	2002/10/22
_	44	<pre>(interrupt adj register))) stor\$4 and hardware and software and (710/260-266.ccls. and ((interrupt adj controller) and (interrupt adj register)))</pre>	USPAT; US-PGPUB	2002/10/23 10:57
_	1	("5459872").PN.	USPAT; US-PGPUB	2002/10/23 08:58
_	1	stor\$4 and 6298410.PN.	USPAT; US-PGPUB	2002/10/23 08:58
-	261	reserv\$4 and stor\$4 and (("5459872").PN.)	USPAT; US-PGPUB	2002/10/23 08:58
_	261	((interrupt adj controller) and (interrupt adj register))	USPAT; US-PGPUB	2002/10/23
_	71	((interrupt adj controller) and (interrupt adj register)) and (stor\$4 adj device)	USPAT; US-PGPUB	2002/10/23
_	0	((interrupt adj controller) and (interrupt adj register)) and (stor\$4 adj device) and (distinguesh\$3 near6 reguests).	USPAT; US-PGPUB	2002/10/23 09:02
_	0	((interrupt adj controller) and (interrupt adj register)) and (stor\$4 adj device) and (distinguish\$3 near6	USPAT; US-PGPUB	2002/10/23 09:03
_	0	requests) ((interrupt adj controller) and (interrupt adj register)) and (stor\$4 adj	USPAT; US-PGPUB	2002/10/23 09:03
_	65	<pre>device) and (distinguish\$3 adj2 requests) ((interrupt adj controller) and (interrupt adj register)) and (stor\$4 adj device) and (hardware and software)</pre>	USPAT; US-PGPUB	2002/10/23 09:03
-	0	((interrupt adj controller) and (interrupt adj register)) and (stor\$4 adj device) and ((hardware and software) near6 request)	USPAT; US-PGPUB	2002/10/23